

## ABSTRACT OF THE DISCLOSURE

A method of and apparatus for merging bit streams for seamless reproduction, and a recording medium for recording control information for bit stream merging. System clock reference (SCR) control information representing whether the SCR value of a bit stream is temporally discontinuous with respect to that of the preceding bit stream at the boundary between the two bit streams is recorded when the two bit streams are being merged. Thus, even where the SCR value at the boundary between merged bit streams rapidly changes when the merged bit stream is reproduced, seamless reproduction is accomplished. The SCR control information includes at least one among a control information identifier, a control information length indicator, a discontinuity indicator, and other information.